



**CHASE ELECTRICS**  
LONDON SW14 7LY  
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GEOMETRIC TOLERANCE SYMBOLS TO BS208		DE-BURR UNLESS OTHERWISE STATED	
STRAIGHTNESS FLATNESS ROUNDNESS PARALLELISM SQUARENESS ANGULARITY RUN-OUT POSITION CONCENTRICITY SYMMETRY DIAMETER DATUM FEATURE		DIMENSIONS IN MILLIMETRES	
TOLERANCES UNLESS OTHERWISE STATED		SCREW THREADS	
ØIMS. TO 2 DEC. PLACES ±0.1	ØIMS. TO 1 DEC. PLACE ±0.2	R.A. TO B.S. 93 NORMAL UNIFIED TO B.S. 1580	
ØIMS. WITHOUT DEC. PLACE ±0.4	HOLE DIAMETER +0.15 -0	ISO METRIC TO B.S. 3643 PT.1,2 or 3	
ANGULAR -0°30'		DIMENSIONS APPLY AFTER PLATING	

MATERIAL/SPECIFICATION	DRAWN PN	10 7 12 94 24128(B.H.) RB
TREATMENT/FINISH	CHECKED	9 14.11.94 24131(B.H.) A.H.
USED ON	TRACED	8 16.3.94 24029(B.H.) A.H.
	PROD	15-02-93 <b>FACTORY ISSUE</b>
	QA	7 7-1-93 92136 KM
	APP.	6 2.9.92 92056 & 92068
	SCALE	2:3 180°
	MASS	lb/Kg

ISSUE	DATE	CHANGE No.	CHK'D	APP'D	ISSUE	DATE	CHANGE No.	CHK'o	APP'd
10	7 12 94	24128(B.H.)	RB		15	16.07.01	0106.01	BDE	PS
9	14.11.94	24131(B.H.)	A.H.		14	15.12.00	C/N00034		
8	16.3.94	24029(B.H.)	A.H.		13	15.11.00	C/N00028		
7	7-1-93	92136	KM		12	070597	97025		
6	2.9.92	92056 & 92068			11	15.06.95	95038(B.H.)	RB	

TITLE:-  
**FINE STEP V.C.O. PCB. CIRCUIT DIAGRAM**

ACTERNA 361 976 S

DRG. No. **C2-3405** SHEET 1 OF 1 SHEETS